

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

 ATTY. DOCKET NO.
43876-144

 SERIAL NO.
10/616,303

 APPLICANT
Craig HANSEN, et al.

 FILING DATE
July 10, 2003

 GROUP
2463-2676

(PTO-1449)

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
MM		US 4,765,393	11/15/1988	Chu et al.	
		US 4,814,978	03/21/1989	Craig C. Hansen, et al.	
		US 5,031,135	07/09/1991	Patel	
		US 5,280,698	01/1994	Osski et al.	
		US 5,481,680	01/02/1996	Dockser	
		US 5,487,024	01/1996	Girardeau Jr.	
		US 5,600,614	02/1997	Gahan et al.	
		US 5,740,093	04/14/1998	Sharangpani	
		US 5,742,840	04/21/1998	Hansen et al.	
		US 5,768,548	08/1998	Kwon	
		US 5,898,849	04/27/1999	Tran	
		US 5,998,057	11/30/1999	Hunter L. Scales, III, et al.	
		US 6,041,404	03/21/2000	Patrice Roussel, et al.	
		US 6,052,769	04/18/2000	Thomas R. Huff, et al.	
		US 6,173,393 B1	01/09/2001	Salvador Palanca, et al.	
MM		US 6,275,834 B1	08/14/2001	Derrick Chu Lin, et al.	
MM		US 6,295,669	09/2001	Hansen et al.	

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code-Number & Kind Codes (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No

OTHER ART (including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
MM		IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications And Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (March 1992)
MM		IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)", IEEE Standards Department, Draft 1.25 IEEE P1596.4-199X (May 1993)
MM		IBM, "The PowerPC Architecture: A Specification For A New Family of Risc Processors", 2nd Ed., Morgan Kaufmann Publishers, Inc., (1994).
MM		Hewlett-Packard Co., "PA-RISC 1.1 Architecture and Instruction Set", Manual Part No. 08740-90038, (1990).
MM		MIPS Computer Systems, Inc., "MIPS R4000 User's Manual", Mfg. Part No. M8-00040, (1990).

EXAMINER

Mackley Hovestine

DATE CONSIDERED

11/27/04

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.



**INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION**

(PTO-1449)

ATTY. DOCKET NO.
43876-144

SERIAL NO.
10/616,303

APPLICANT
HANSEN, et al.

FILING DATE
July 10, 2003

GROUP
2483-2676

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
<i>MM</i>	4,025,772	05/24/77	Constant			
	4,489,393	12/18/84	Kawahara, et al.			
	4,701,875	10/20/87	Konishi, et al.			
	4,727,505	02/23/88	Konishi, et al.			
	4,876,660	10/24/89	Owens, et al.			
	4,893,267	01/09/90	Alsup, et al.			
	4,956,801	09/11/90	Priem et al.			
	4,969,118	11/06/90	Montoye, et al.			
	4,975,868	12/04/90	Freerksen			
	5,032,865	07/16/91	Schlunt			
	5,157,388	10/20/92	Kohn			
	5,201,056	04/06/93	Daniel, et al.			
<i>MM</i>	5,268,855	12/07/93	Mason, et al.			
<i>MM</i>	5,268,995	12/07/93	Diefendorff, et al.			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>MM</i>	Parallel Computers for Graphics Applications, Adam Levinthal, Pat Hanrahan, Mike Paquette, Jim Lawson, Pixar San Rafael, California, 1987
	Organization of the Motorola 88110 Superscalar RISC Microprocessor, Keith Diefendorff and Michael Allen.
<i>MM</i>	Microprocessor Report, Volume 7 Number 13, October 4, 1993, IBM Regains Performance Lead with Power2, Six Way Superscalar CPU in MCM Achieves 126 SPECint92.
	IBM Creates PowerPC Processors for AS/400, Two New CPU's Implement 64-Bit Power PC with Extensions by Linley Gwennap, July 31, 1995.

EXAMINER

Hackley Monestime

DATE CONSIDERED

11/17/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)		ATTY. DOCKET NO. 43876-144		SERIAL NO. 10/616,303			
		APPLICANT HANSEN, et al.					
		FILING DATE July 10, 2003		GROUP 2483 2676			
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
MM	5,408,581	04/18/95	Suzuki, et al.				
	5,423,051	06/06/95	Fuller, et al.				
	5,426,600	06/20/95	Nakagawa, et al.				
	5,500,811	03/19/96	Corry				
	5,557,724	09/17/96	Sampat, et al.				
	5,588,152	12/24/96	Dapp, et al.				
	5,592,405	01/07/97	Gove, et al.				
	5,640,543	06/17/97	Farrell, et al.				
	5,642,306	06/24/97	Mennemeier, et al.				
	5,666,298	09/09/97	Peleg, et al.				
	5,669,010	09/16/97	Duluk, Jr.				
	5,673,407	09/30/97	Poland, et al.				
	MM	5,675,526	10/07/97	Peleg, et al.			
MM	5,680,338	10/21/97	Agarwal, et al.				
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
MM	0 474 246 A2	06/09/91	Japan				
MM	0 654 733 A1	05/07/94	Germany				
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
MM	The Visual Instruction Set (VIS) in UltraSPAR™, L. Kohn, G. Maturana, M. Tremblay, A. Prabhu, G. Zyner, May 3, 1995.						
	Osborne McGraw-Hill, i860™ Microprocessor Architecture, Neal Margulis, Foreword by Les Kohn						
MM	A General-Purpose Array Processor for Seismic Processing, Nov-Dec., 1984, Volume 1, No. 3) Revisiting past digital signal processor technology, Don Shaver- Jan-Mar. 1998.						
MM	Accelerating Multimedia with Enhanced Microprocessors, Ruby B. Lee, 1995.						
EXAMINER			DATE CONSIDERED				
<i>Jacky Honestine</i>			<i>11/17/04</i>				

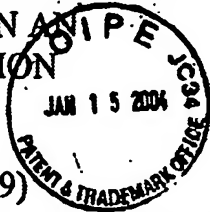
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 43876-144		SERIAL NO. 10/616,303	
				APPLICANT HANSEN, et al.			
				FILING DATE July 10, 2003		GROUP 2483 2678	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
	5,721,892	02/24/98	Peleg, et al.				
	5,734,874	03/31/98	Van Hook, et al.				
	5,757,432	05/26/98	Dulong, et al.				
	5,758,176	05/26/98	Agarwal, et al.				
	5,802,336	09/01/98	Peleg, et al.				
	5,809,292	09/15/98	Wilkinson, et al.				
	5,818,739	10/06/98	Peleg, et al.				
	5,825,677	10/20/98	Agarwal, et al.				
	5,835,782	11/10/98	Chu Lin, et al.				
	5,886,732	03/23/99	Humpleman				
	5,922,066	07/13/99	Cho, et al.				
	5,983,257	11/09/99	Dulong, et al.				
	6,016,538	01/18/00	Gutttag, et al.				
	6,092,094	07/18/00	Ireton				
	6,401,194 B1	06/04/02	Nguyen, et al.				
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER Hackley Monestime				DATE CONSIDERED 11/17/04			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(PTO-1449)

ATTY. DOCKET NO.
43876-144SERIAL NO.
10/616,303APPLICANT
Craig HANSEN, et al.FILING DATE
July 10, 2003GROUP
2483 2676

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
HA		US 5,819,101	10/06/1998	Peleg et al.	
HA		US 5,881,275	03/09/1999	Peleg et al.	
HA		US 6,119,216	09/12/2000	Peleg et al.	
HA	B1	US 6,518,406	02/04/2003	Peleg et al.	
HA	B1	US 6,539,487	03/25/2003	Anderson et al.	
HA	B1	US 6,574,724	09/03/2003	Hoyle et al.	
HA	B2	US 6,631,389	10/07/2003	Lin et al.	
		US			
		US			
		US			
		US			
		US			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes - Number - Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

EXAMINER

DATE CONSIDERED

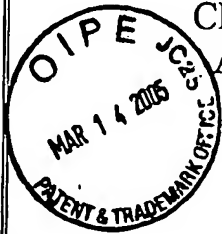
Hackly Monestime

11/17/04

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

INFORMATION DISCLOSURE CITATION IN AN APPLICATION



(PTO-1449)

 ATTY. DOCKET NO.
43876-144

 SERIAL NO.
10/616,303

 APPLICANT
Craig HANSEN, et al.

 FILING DATE
July 10, 2003

 GROUP
2183

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
MM		US 5,508,951	04/16/1996	Ishikawa	
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number + Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
MM		EP 0 657 804 A1	08/14/95	Hewlett-Packard Company		
MM		EP 0 766 169 A1	4/2/1997	Matsushita Electric Industrial Co., Ltd.		

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		RUBY B. LEE, "Subword Parallelism with Max-2," IEEE Micro August 1996, pages 51-58.
		KOUHEI NADEHARA, ICHIRO KURODA, MASAYUKI DAITO, TAKASHI NAKAYAMA, "Low-Power Multimedia RISC," IEEE Micro December 1995, p.20-29

EXAMINER

Hackley Monistone

DATE CONSIDERED

4/28/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.